

PATENT

jc997 U.S. PTO
10/055445
01/23/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Potts

Serial No. not assigned

Filed: January 23, 2002

For: Semiconductor Wafer With Grouped Integrated Circuit Die
Having Inter-Die Connections For Group Testing

(Attny docket: TI-27832)

Group Art Unit: not assigned

Examiner: not assigned

3/10/05
7.3 step 10e
5-28

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

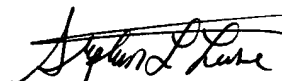
Dear Sir:

Applicant wishes to bring the reference listed on the enclosed PTO-1449A to the attention of the Patent and Trademark Office relative to the subject application. It is respectfully submitted that the Applicant's invention defines patentable subject matter over the teachings of the reference cited in the enclosed PTO-1449A.

A copy of the reference is filed herewith. The cited reference is in the English language. As such, no additional statement of relevance is provided herewith.

Consideration of this information in the prosecution of the above-referenced application, and favorable action on this application, are respectfully requested. This Disclosure Statement, filed in accordance with 37 C.F.R. § 1.97, should not be construed as a representation that a search has been made, or that no other material information, as defined in 37 C.F.R. § 1.56(a), exists.

Respectfully submitted,



Anderson, Levine & Lintel, L.L.P.
12160 Abrams Road, Suite 111
Dallas, Texas 75243-4523
(972) 664-9552

Stephen L. Levine
Registry No. 33,413
Attorney for Applicant

Express Mail label number under 37 C.F.R. § 1.10:
EJ502271574US

jc997 U.S. PTO
10/055445

01/23/02

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	not assigned
				Filing Date	Jan. 23, 2002
				First Named Inventor	Potts
				Art Unit	not assigned
				Examiner Name	not assigned
				Attorney Docket Number	TI-27832
Sheet	1	of	1		

[illegible][illegible]

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

¹ Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231